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		Application Number	10/824/132	
INFO	RMATION DISCLOSURE	Filing Date	04/14/2004	
STATEMENT BY APPLICANT (Upo as many atlants as necessary)		First Named Inventor	YI-Nien Su et al.	
		Art Unit	2891	
		Examiner Name	Victor V. Yevsikov	
Sheet	af	Attorney Docket Number	N1085-00265	

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14		A. KAJITA, et al., Highly Reliable Cultow-k Dual-Damascens interconnect Technology with Hybrid (PAE/SiOC) Dialectrics for 65 nm-node High Performance eDRAM, Proceedings of the IEEE 2003 international interconnect Technology Conference, June 2003	,—
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